

As a below-named inventor, I hereby declare that:

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Inventor's Signature: Harry Williams 8-16-95  
Date  
Residence: 6410 Randolph Drive, Boise, Idaho 83709  
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Post Office Address: 8000 S. Federal Way, Boise, Idaho 83706

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: ) Group:  
Gary Gilliam ) Art Unit:  
 ) Examiner:  
Serial No.: )  
 )  
Filed: Filed Herewith ) POWER OF ATTORNEY BY ASSIGNEE  
 )  
For: ON-CHIP SUBSTRATE REGULATOR )  
TEST MODE )

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., Assignee of the above-identified application by Assignment dated 08/16/95 hereby appoints Lia M. Pappas, (Reg. No. 34,095) and also the members of the firm of HAVERSTOCK & ASSOCIATES a firm including Thomas B. Haverstock (Reg. No. 32,571) and Jonathan O. Owens (Reg. No. 37,902), 260 Sheridan Avenue, Suite 420, Palo Alto, California 94306, (415) 833-0160, as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

Thomas B. Haverstock  
HAVERSTOCK & ASSOCIATES  
260 Sheridan Avenue, Suite 420  
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I hereby certify that the Assignment document filed with the application or filed subsequent to the filing date of the application, has been reviewed and I hereby certify that, to the best of my knowledge and belief, title is with MICRON TECHNOLOGY, INC.

MICRON TECHNOLOGY, INC.

Dated: August 29, 1995

By: W. Bryan Farney  
Name: W. Bryan Farney  
Title: Vice President of Legal Affairs and General Counsel

**S/N 08/520,818**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Gary R. Gilliam	Examiner:	Unknown
Serial No.:	08/520,818	Group Art Unit:	2213
Filed:	August 30, 1995	Docket:	303.221US1
Title:	ON-CHIP SUBSTRATE REGULATOR TEST MODE		

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**REVOCATION/POWER OF ATTORNEY**  
**CERTIFICATE UNDER 37 CFR 3.73(b)**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In accordance with 37 C.F.R. Section 1.32, M.P.E.P. Section 402.05, 402.07, and 324 please revoke any existing Powers of Attorney, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark

Office in connection therewith:

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